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[2885/77]

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Inventor(s)

Martin VORBACH et al.

Serial No.

10/764,159

Filing Date

January 23, 2004

For

METHOD OF HIERARCHICAL CACHING OF CONFIGURATION DATA HAVING DATAFLOW PROCESSORS AND MODULES HAVING TWO- OR MULTIDIMENSIONAL PROGRAMMABLE CELL

STRUCTURE (FPGAs, DPGAs, etc.)

Group Art Unit

2186

Examiner

Stephen C. Elmore

Confirmation No.

8160

Mail Stop Amendment Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Mail Stop Amendment, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-

1450 on 3 MM 12 2005

Michelle M. Carniaux (Reg. No. 36,098)

TRANSMITTAL

SIR:

Please find an Amendment transmitted herewith for filing in the above-identified patent application.

Applicants hereby request a three-month extension of time for responding to the Office Action of November 4, 2004. The extended period for response expires on May 4, 2005. Please charge the \$510.00 extension fee and any other fee that may be required to Deposit Account No. 11-0600. A duplicate of this Transmittal is enclosed.

Respectfully submitted,

KENYON & KENYON

Dated: 3 MM 2005

Michelle Carniaux

Reg. No. 36,098 One Broadway

New York, NY 10004

(212) 425-7200

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